

Information Disclosure Statement

New U.S. Patent Application for
METHOD FOR FABRICATING METAL-OXIDE
SEMICONDUCTOR TRANSISTOR
Our Ref. No.: P03H1035/US/cj

Reference No.:

(1) Dual-Workfunction Gate Engineering in a Corner Parasitic-Free Shallow-Trench-Isolation Complementary-Metal-Oxide-Semiconductor Technology

(Japanese Journal of Applied Physics, Vol. 38, 1999 Publication Board, Pages 2232-2237)

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LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

ATTY. DOCKET NO.: P68985US0 GROUP ART UNIT: Not Yet Assigned
 SERIAL NO.: New Application FILING DATE: July 14, 2003
 APPLICANT(S): Sang-Don LEE TODAY'S DATE: July 14, 2003

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	INT'L CLASS	SUB- CLASS	FILING DATE (If Appropriate)
AA						
AB						

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION (YES) (NO)
AC						
AD						
AE						

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

AF : Udo SCHWALKE, et al.; "Dual-Workfunction Gate Engineering in a Corner Parasitics-Free Shallow-Trench-Isolation Complementary-Metal-Oxide-Semiconductor Technology"; Jpn. J. Appl. Phys. Vol. 38; 1999; pp.2232-2237.

AG : _____

EXAMINER

DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Sang-Don LEE
Serial No.: New Application Group Art Unit: Not Yet Assigned
Filed: July 14, 2003 Examiner: Not Yet Assigned
Title: METHOD FOR FABRICATING METAL-OXIDE SEMICONDUCTOR TRANSISTOR

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner for Patents
PO Box 1450,
Alexandria, VA 22313

July 14, 2003

Sir:

As a means of complying with the duty of disclosure under 37 CFR §1.56, and in accordance with 37 CFR §§1.97 and 1.98, Applicant(s), through the undersigned attorney, submits this Information Disclosure Statement. The patents, publications or other information submitted herewith are listed on the attached Form PTO-1449 and copies are attached.

In accordance with 37 CFR §1.97(b)(1) or (2), this Information Disclosure Statement is being filed either within three months of the filing date of the above-identified application, or within three months of the date of entry into the national stage of the above-identified application as set forth in 37 CFR §1.491. Accordingly, no fee is required.

Respectfully submitted,

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